

**Amendments to the Claims**

**IN THE CLAIMS:**

This listing of claims will replace all prior versions, and listings, of claims regarding the present application. In reading this, text added by the amendment is underlined and canceled text appears in ~~strikethrough~~ or ~~[[double brackets]]~~.

1        1.        (Currently amended) A flash memory card interfacing system for detachably coupling to  
2 a host computer system, the flash memory card interfacing system also configured for performing  
3 data storage and control operations, the flash memory card interfacing system comprising: a  
4 device for connecting a flash memory card to a USB port, such that the flash memory card  
5 operates as a removable data storage for the host computer system.

1        2.        (Currently amended) A flash memory card interfacing system for coupling a flash  
2 memory ~~device~~ card to a USB bus, the flash memory card interfacing system comprising:  
3            a.        a flash memory module for executing a write operation, a read operation, and an  
4                      erase operation;  
5            b.        an interface device for coupling the flash memory card to a USB port;  
6            c.        a USB connector for connecting to the USB bus, sending and receiving signals,  
7                      and coupling the interface device to the flash memory card;  
8            d.        a flash controller coupled to the flash memory module and the USB connector, the  
9                      flash controller controlling the USB connector and configuring the flash memory  
10                     card to a selected operating mode of the interface device; and  
11            e. ~~[[f.]]~~    flash interface circuitry coupled to the flash controller, the flash interface circuitry  
12                     identifying the selected operating mode of the interface device.

1        3.        (Original) The flash memory card interfacing system of claim 2, wherein the selected  
2 operating mode is a universal serial bus mode.

1        4.        (Currently amended) The flash memory card interfacing system of claim 2, further  
2 comprising a fifty pin connector end configured to couple to ~~[[an]]~~ the interface device.

1        5.        (Currently amended) The flash memory card interfacing system of claim 2, further  
2 comprising a sixty-eight pin connector end configured to couple to [[an]] the interface device.

1        6.        (New) The flash memory card interfacing system of claim 2, wherein the flash interface  
2 circuitry is mounted on the flash memory card.

1        7.        (New) The flash memory card interfacing system of claim 2, wherein the flash memory  
2 card interfacing system further comprises an adapter module coupled to the flash memory card  
3 and the interface device.

1        8.        (New) The flash memory card interfacing system of claim 7, wherein the flash interface  
2 circuitry is mounted within the adapter module.

1        9.        (New) A flash memory card interfacing system for detachably coupling to a host  
2 computer system, the flash memory card interfacing system also configured for performing data  
3 storage and control operations, the flash memory card interfacing system comprising: a device for  
4 connecting a flash memory card to a USB port, such that the flash memory card automatically  
5 configures itself to cooperatively operate in a selected operating mode through the device.

1        10.       (New) The flash memory card interfacing system of claim 9, wherein the selected  
2 operating mode is a universal serial bus mode.

1        11.       (New) The flash memory card interfacing system of claim 9, further comprising a fifty  
2 pin connector end configured to couple to the device.

1        12.       (New) The flash memory card interfacing system of claim 9, further comprising a sixty-  
2 eight pin connector end configured to couple to the device.

1        13.       (New) The flash memory card interfacing system of claim 9, wherein the flash memory  
2 card is powered by the host computer system.

1        14.       (New) A flash memory card interfacing system for coupling a flash memory card

2 to a USB bus, the flash memory card interfacing system comprising:

- 3       a.       the flash memory card for executing a write operation, a read operation, and an  
4               erase operation and comprising: an interface device for coupling the flash memory  
5               card to a USB port; a flash controller coupled to the flash memory module and a  
6               USB connector, the flash controller controlling the USB connector and  
7               configuring the flash memory card to a universal serial bus mode of the interface  
8               device; and flash interface circuitry coupled to the flash controller, the flash  
9               interface circuitry identifying the universal serial bus mode of the interface device;  
10              and  
11       b.       the USB connector for connecting to the USB bus, sending and receiving signals,  
12              and coupling the interface device to the flash memory card.

1       15.     (New) The flash memory card interfacing system of claim 14, further comprising a fifty  
2       pin connector end configured to couple to the interface device.

1       16.     (New) The flash memory card interfacing system of claim 14, further comprising a sixty-  
2       eight pin connector end configured to couple to the interface device.